



PATENT APPLICATION RECEIVED

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In re application of:

Yoshinori OKAJIMA

Application No.: 09/372,166

Filed: August 11, 1999

Group Art Unit: 2818

Examiner: Trong Phan

Atty. Dkt. No.: 100021-09029

For: ELECTRONIC CIRCUIT SYSTEM, AND SIGNAL TRANSMISSION METHOD,
TO IMPROVE SIGNAL TRANSMISSION EFFICIENCY AND SIMPLIFY SIGNAL
TRANSMISSION MANAGEMENT

RESPONSE UNDER 37 CFR § 1.111

Commissioner for Patents
Washington, D.C. 20231

October 18, 2001

Sir:

This is a full and complete response to the non-final Office Action dated July 18, 2001, setting a three month shortened statutory time period for reply. A response is due by October 18, 2001.

IN THE CLAIMS

Please amend claims 1, 17, 29 and 34-36 as follows. A marked-up copy of the claims showing the changes made below are submitted herewith.

1. (Amended) An electronic circuit system comprising:
- at least three macro circuits each including a logic circuit or a memory circuit, and
- having a plurality of input terminals and a plurality of output terminals; and

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